

## **“Maintaining the self-fulfilling prophecy: from dimensional to functional scaling”**

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Julien Ryckaert received the M.Sc. degree in Electrical Engineering from the University of Brussels (ULB), Belgium, in 2000 and the PhD degree from the Vrije Universiteit Brussel (VUB) in 2007. He joined IMEC (Leuven, Belgium) as a mixed-signal designer in 2000 specializing in RF transceivers, ultra-low power circuit techniques and analog-to-digital converters. In 2010 he joined the process technology division in charge of design enablement for 3D and photonics technologies. Since 2013, he is in charge of IMEC’s Design-Technology Co-Optimization (DTCO) platform for advanced CMOS technology nodes. His current main area of research is the 5nm technology node and beyond.

Julien Ryckaert has authored and co-authored more than 50 peer-reviewed Journal and conference papers. He has served as TPC member of the Asian Solid-State circuits Conference.